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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/058,116	01/29/2002	Mutsumi Kimura	111829	1207
25944	7590	03/25/2004	EXAMINER	
OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320			MUNSON, GENE M	
			ART UNIT	PAPER NUMBER
			2811	

DATE MAILED: 03/25/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/58,116

Applicant(s)

M. KIMURA

Examiner

G. MUNSON

Group Art Unit

2811

— The MAILING DATE of this communication appears on the cover sheet beneath the correspondence address —

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE THREE MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, such period shall, by default, expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- ☒ Responsive to communication(s) filed on 2 January, 11 March 2004
- ☐ This action is **FINAL**.
- ☐ Since this application is in condition for allowance except for formal matters, **prosecution as to the merits is closed** in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 1 1; 453 O.G. 213.

Disposition of Claims

- ☒ Claim(s) 1-5, 9-15 is/are pending in the application.
- Of the above claim(s) _____ is/are withdrawn from consideration.
- ☒ Claim(s) 1-5, 9, 10 is/are allowed.
- ☒ Claim(s) 11-15 is/are rejected.
- ☐ Claim(s) _____ is/are objected to.
- ☐ Claim(s) _____ are subject to restriction or election requirement

Application Papers

- ☐ The proposed drawing correction, filed on _____ is ☐ approved ☐ disapproved.
- ☐ The drawing(s) filed on _____ is/are objected to by the Examiner
- ☐ The specification is objected to by the Examiner.
- ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119 (a)-(d)

- ☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119 (a)-(d).
- ☐ All ☐ Some* ☐ None of the:
- ☐ Certified copies of the priority documents have been received.
- ☐ Certified copies of the priority documents have been received in Application No. _____
- ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a))

*Certified copies not received: _____

Attachment(s)

- ☐ Information Disclosure Statement(s), PTO-1449, Paper No(s). _____
- ☐ Interview Summary, PTO-413
- ☐ Notice of Reference(s) Cited, PTO-892
- ☐ Notice of Informal Patent Application, PTO-152
- ☐ Notice of Draftsperson's Patent Drawing Review, PTO-948
- ☐ Other _____

Office Action Summary

Art Unit: 2811

Examination is continued under 37 CFR 1.114.


Claims 1-5, 9 and 10 are allowed over the art of record.

Claims 11-15 are rejected under 35 U.S.C. 112, first, second and fourth paragraphs. It is unclear how claims 11-13 and 15 further limit the subject matter of the "semiconductor device" of claim 1, and the claims are confusing. As claimed, the "circuit board" (claim 11), "electro-optical device" (claims 12, 13) and "electronic apparatus" (claim 15) are not the "semiconductor device". It is suggested that claims 11-13 and 15 each be put in completed form as independent claims including all limitations of claims 1, 11; 1, 11,12; 1, 13; 1, 11, 12, 15.

The specification does not clearly describe the "circuit board" (claims 11, 12) and "shift registers, level shifters, buffer circuits, and analog switches" (claim 13) so as to enable any person skilled in the art to make and use the claimed inventions with the "semiconductor device" of claim 1.

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3/18/04


GENE M. MUNSON
EXAMINER
GROUP ART UNIT 2811